IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
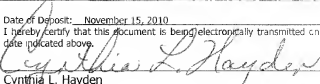
In re Applicant:	§	Art Unit: 2621
Louis Lippincott et al.	§	
	§	Examiner: Chikaodili E. Anyikire
Serial No.: 10/750,075	§	
	§	Conf. No.: 2083
Filed: December 31, 2003	§	
	§	Docket: ITL.1703US
For: Motion Estimation Sum of all	§	P17498
Differences (SAD) Array Having	§	
Reduced Semiconductor Die	§	Assignee: Intel Corporation
Area Consumption	§	

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed September 1, 2010, please amend the above-referenced patent application as follows: